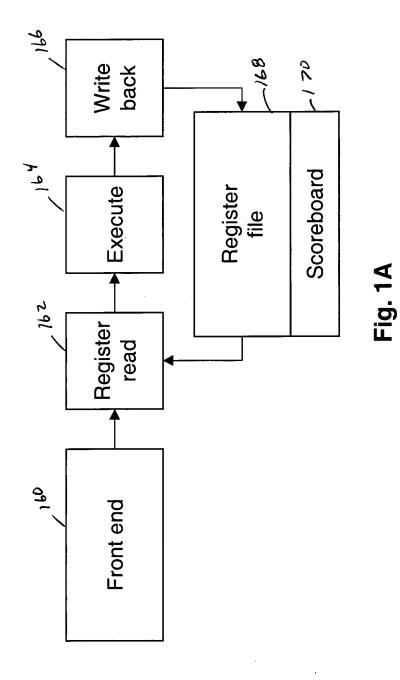


Fig. 1



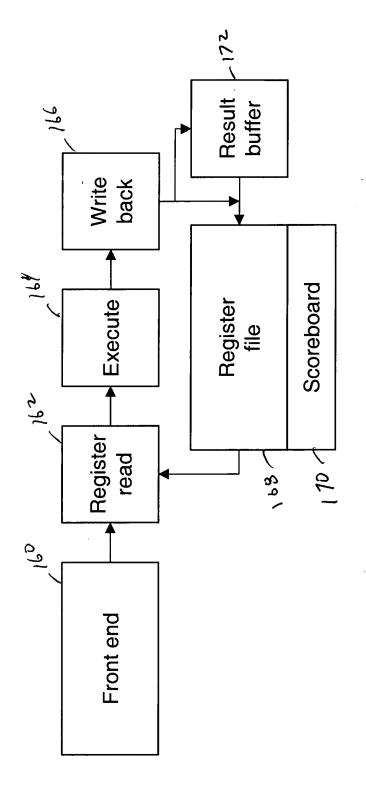


Fig. 1B

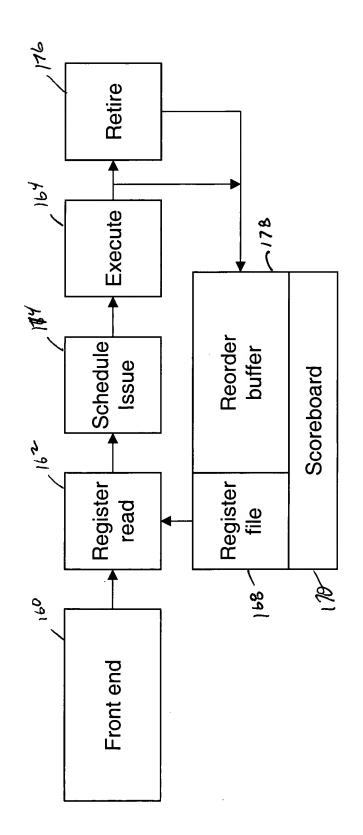


Fig. 1C

INST#	INSTRUCTION	DESCRIPTION
1: 2: 3: 4: (P1)	mov 0 -> R1 ld (mem) -> R2 cmp 0, R2 -> P1 add 5, R1 -> R1	# establishes R1 # loads R2 from memory # compares R2 to 0 and sets resulting predicate P1 # adds 5 to R1 if P1 is true
5:	sub 10, R1 -> R3	# sets R3 with the difference of 10 and R1

Fig. 2

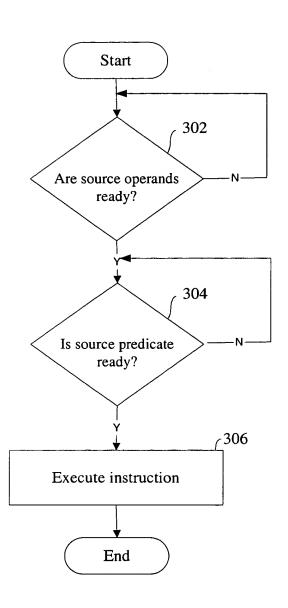


FIG. 3A

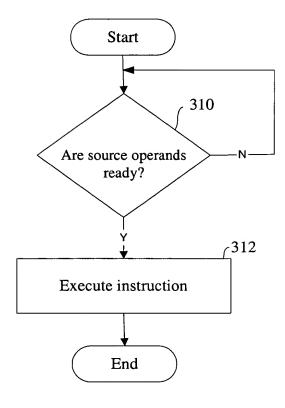


FIG. 3B

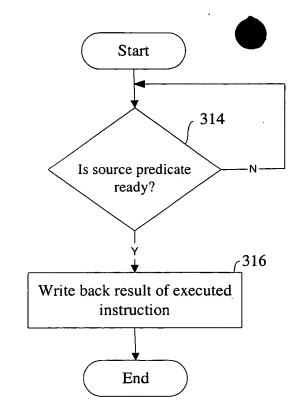


FIG. 3C

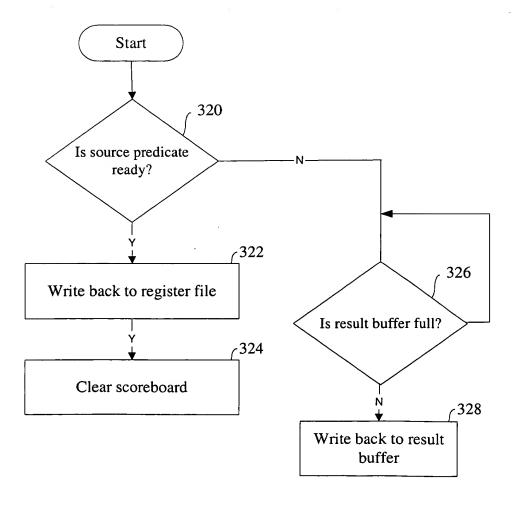


FIG. 3D

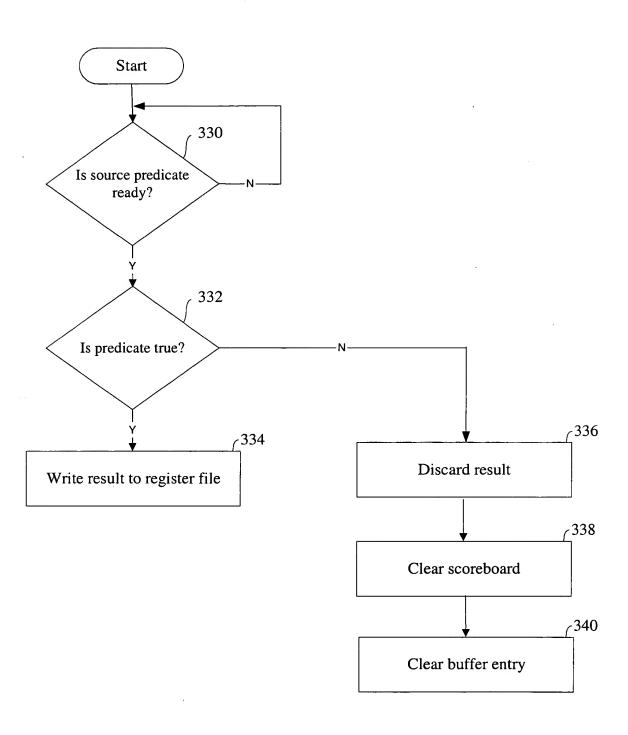


FIG. 3E

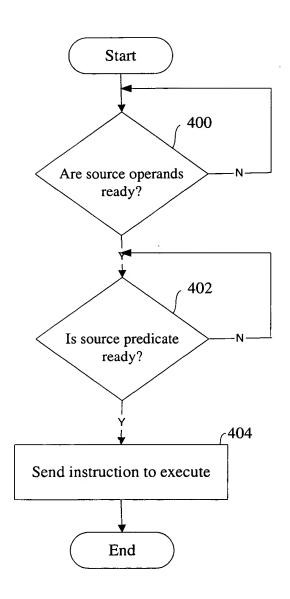


FIG. 4A

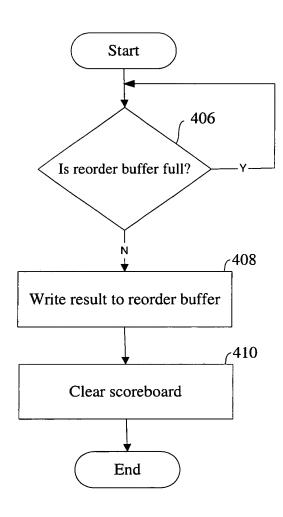


FIG. 4B

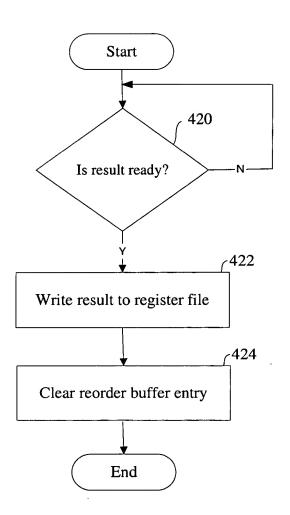


FIG. 4C

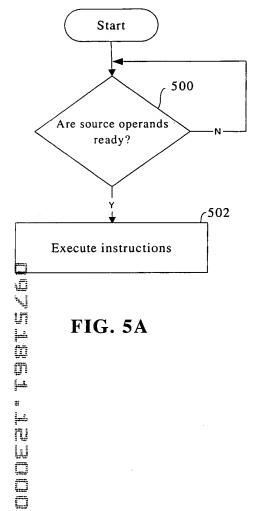


FIG. 5A

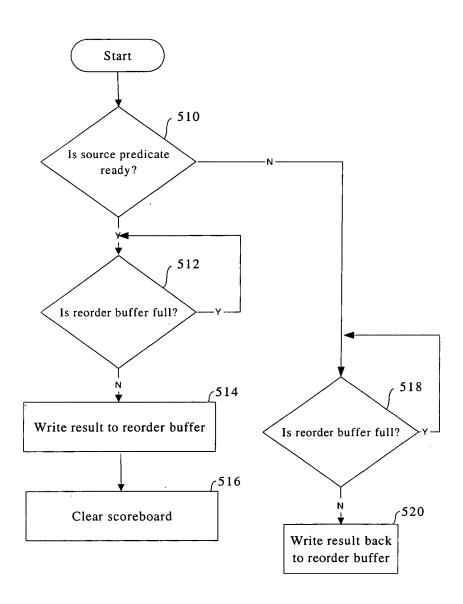


FIG. 5B

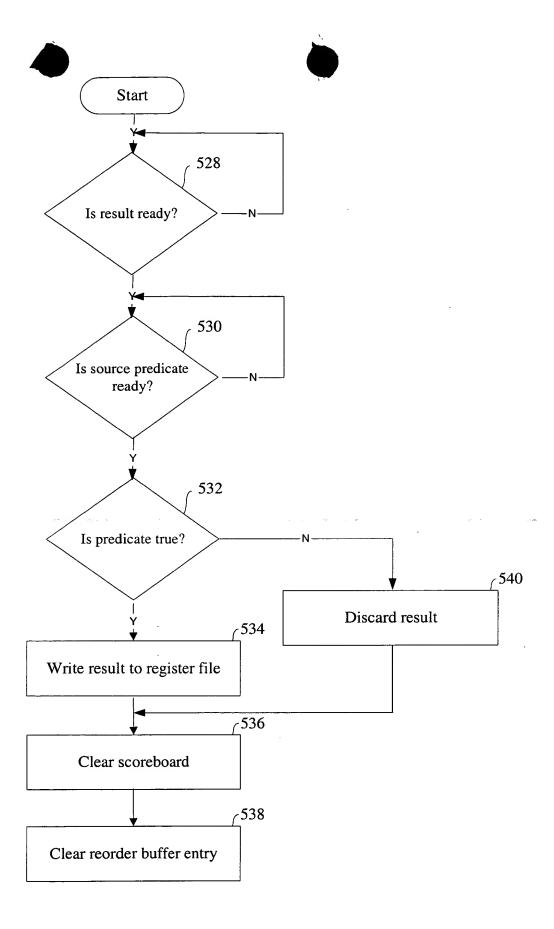


FIG. 5C